

# Model Name: GA-P85-D3

2.0

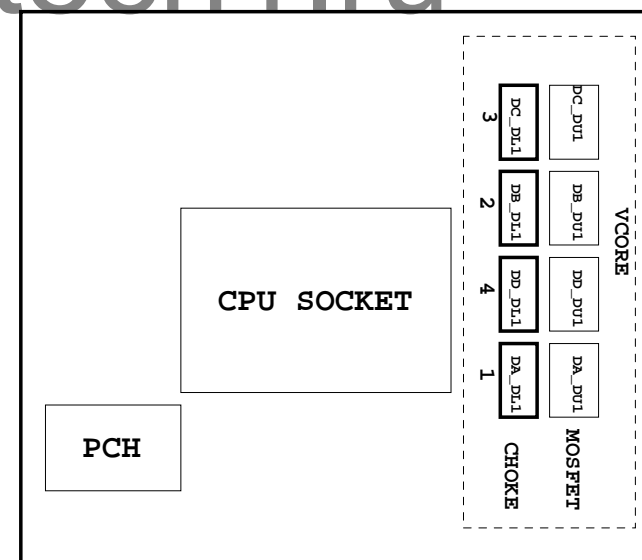
SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE
10	PCH_RGB,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCIEX4 SLOT
16	ITE8892 PCI BRIDGE
17	PCI SLOT 1~4
18	I/O ITE8620
19	COM, -PROHOT, R_USB
20	Dual BIOS / LPT
21	ALC887 CODEC
22	REAR AUDIO JACK
23	VCORE_ ISL95820_1
24	VCORE_ ISL95820_2
25	DDR15V / M3 POWER
26	NCP3933 OVER VOLTAGE
27	DISCRETE POWER

SHEET TITLE

28	F_PANEL , F_USB2.0/3.0
29	ATX POWER, CLOCK GEN
30	HWM , KB/MS , FAN CTRL
31	Realtek 8111F-VL
32	HDMI
33	TABLE LIST
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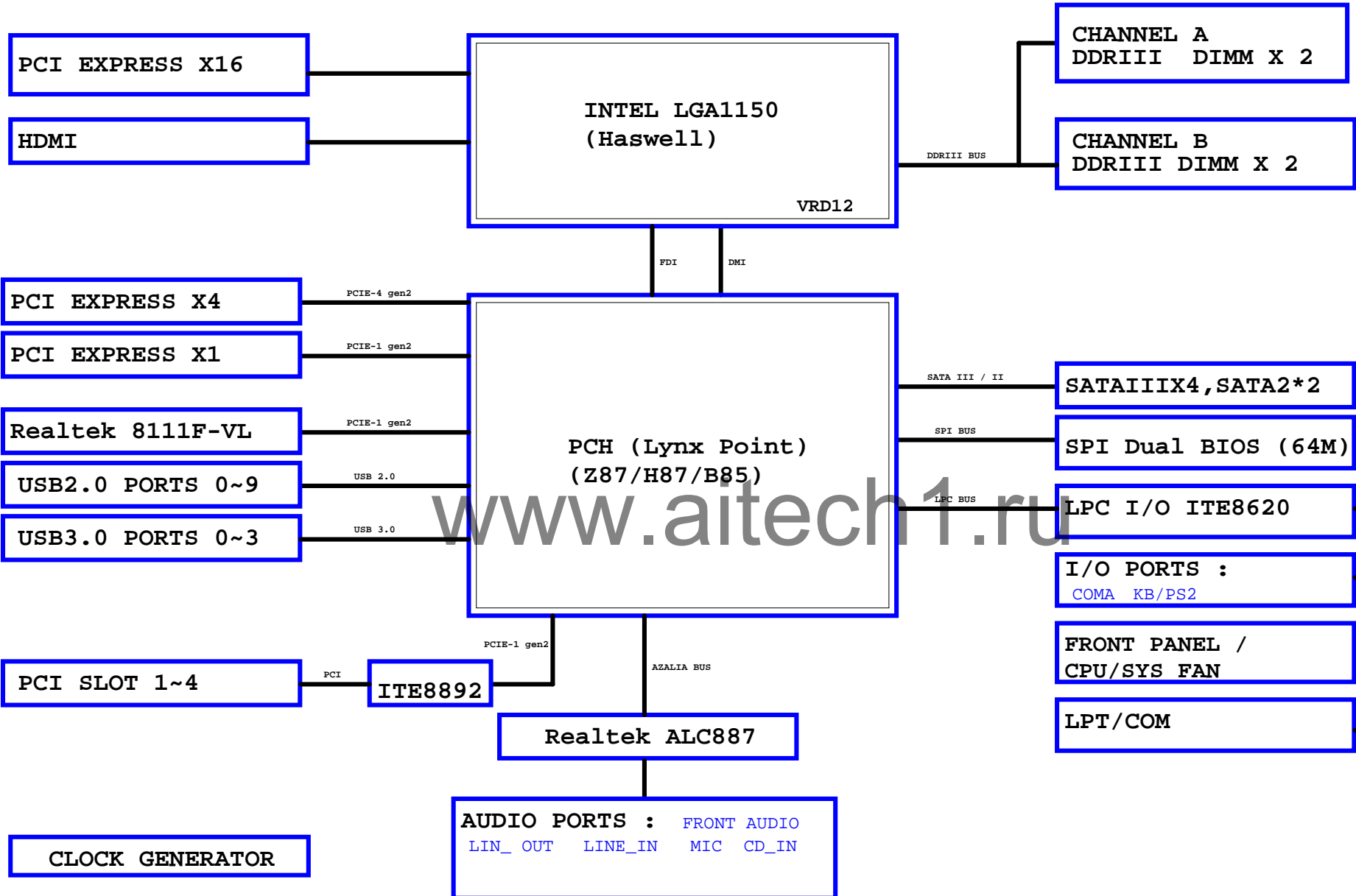


<b>Gigabyte Technology</b>			
Title: Cover Sheet			
Size: Custom	Document Number: GA-P85-D3	Rev: 2.0	
Date: Wednesday, February 26, 2014	Sheet: 1	of 33	

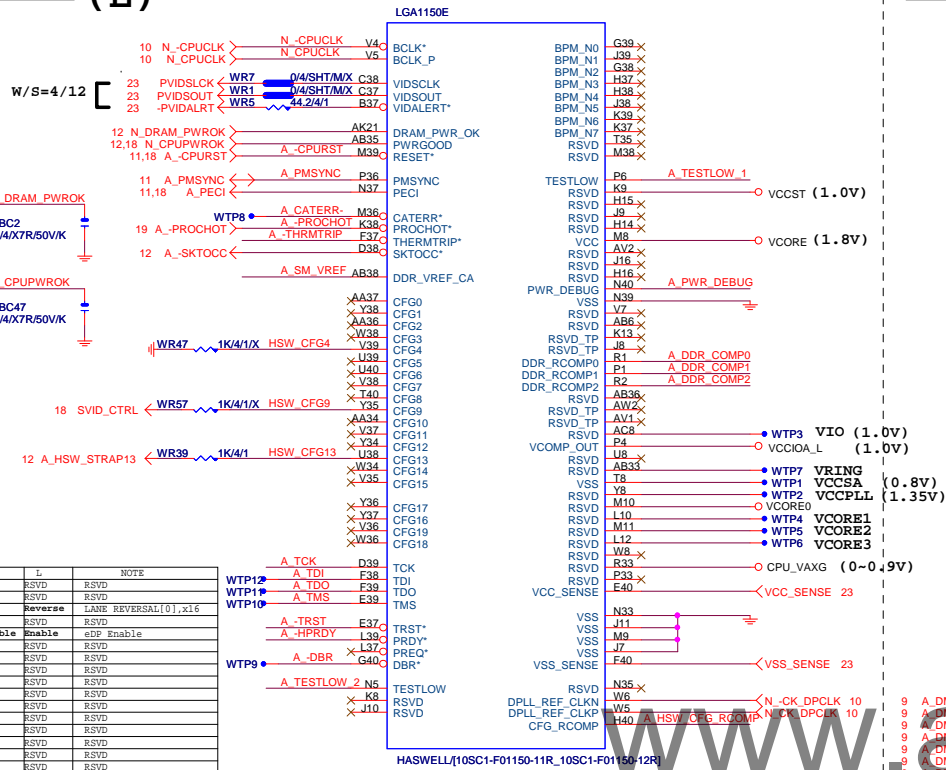
## Component value change history

[illegible][illegible]

## BLOCK DIAGRAM



**LGA1150 (E)**

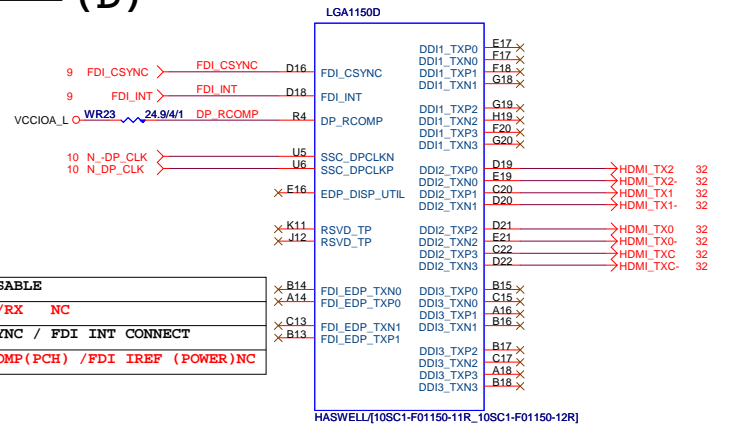


CFG	H	L	NOTE
0	RSVD	RSVD	RSVD
1	RSVD	RSVD	RSVD
2	WOM	Reverse	DATA REVERSAL[0],x16
3	RSVD	RSVD	RSVD
4	Disable	Enable	eDP Enable
7	RSVD	RSVD	RSVD
8	RSVD	RSVD	RSVD
9	RSVD	RSVD	RSVD
10	RSVD	RSVD	RSVD
11	RSVD	RSVD	RSVD
12	RSVD	RSVD	RSVD
13	RSVD	RSVD	RSVD
14	RSVD	RSVD	RSVD
15	RSVD	RSVD	RSVD
16	RSVD	RSVD	RSVD
17	RSVD	RSVD	RSVD

CFG6	CFG5	PCIE CONFIG
1	1	1x16 , Default
1	0	2X8
0	1	RSVD
0	0	X8,X4,X4

CFG 0-17 all internal PULL-UP

**LGA1150 (D)**



FDI DISABLE
FDI TX/RX NC
FDI CSYNC / FDI INT CONNECT
FDI RCOMP(PCH) /FDI IREF (POWER)NC

FDI:4/4/4//15(breakout min 4/4/4//8)  
Impedance=85 +- 15%

DP/HDMI 4/4/4//20                      FDI 4/4/4/12

Impedance=85 +- 15%

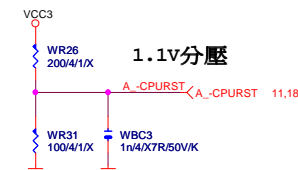
LGA1155 (C)



CPU PEG 5/5/5//20 Impedance=80 +- 15%

DMI 4/4/4//15 Impedance=85 +- 15%

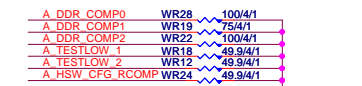
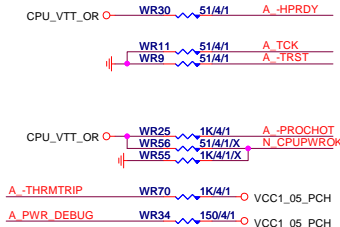
**-CPURST**



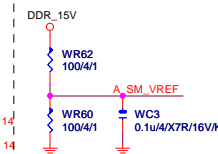
## CPU SVID



CPU	PU/PD
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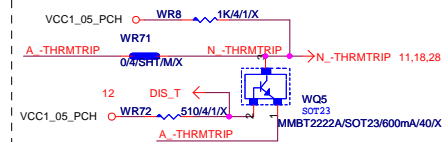
SM REF
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| THRMTRIP DISABLE FOR Z87 OVERCLOCK

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## Gigabyte Technology

Title			
CPU LGA1150-A			
Size	Document Number	Rev	
Custom	GA-P85-D3	2.0	
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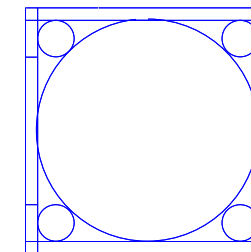
LGA1150A					
MAAA0	AU13	DDR0_M0	DDR0_D00	AD38	MDA0
MAAA1	AV16	DDR0_M1	DDR0_D01	AD39	MDA1
MAAA2	AU17	DDR0_M2	DDR0_D02	AD38	MDA2
MAAA3	AW17	DDR0_M3	DDR0_D03	AF39	MDA3
MAAA4	AU18	DDR0_M4	DDR0_D04	AD37	MDA4
MAAA5	AW18	DDR0_M5	DDR0_D05	AD40	MDA5
MAAA6	AV17	DDR0_M6	DDR0_D06	AF37	MDA6
MAAA7	AT18	DDR0_M7	DDR0_D07	AE40	MDA7
MAAA8	AU18	DDR0_M8	DDR0_D08	AH40	MDA8
MAAA9	AT19	DDR0_M9	DDR0_D09	AH39	MDA13
MAAA10	AW11	DDR0_M10	DDR0_D10	AH38	MDA10
MAAA11	AV19	DDR0_M11	DDR0_D11	AH39	MDA11
MAAA12	AU19	DDR0_M12	DDR0_D12	AH37	MDA12
MAAA13	Y10	DDR0_M13	DDR0_D13	AH38	MDA8
MAAA14	AT20	DDR0_M14	DDR0_D14	AH37	MDA14
MAAA15	AU21	DDR0_M15	DDR0_D15	AH40	MDA9
		DDR0_M16	DDR0_D16	AP39	MDA21
MODT_A0	AW10	DDR0_OTD0	DDR0_D17	AP38	MDA18
MODT_A1	AY8	DDR0_OTD1	DDR0_D18	AP39	MDA19
MODT_A2	AW9	DDR0_OTD2	DDR0_D19	AP39	MDA17
MODT_A3	AU8	DDR0_OTD3	DDR0_D20	AP38	MDA16
		DDR0_OTD1	DDR0_D21	AP37	MDA22
		DDR0_OTD2	DDR0_D22	AP40	MDA25
	AW33	DDR0_ECC0	DDR0_D23	AP40	MDA23
	AV33	DDR0_ECC1	DDR0_D24	AV37	MDA24
	AU31	DDR0_ECC2	DDR0_D25	AW37	MDA29
	AV31	DDR0_ECC3	DDR0_D26	AU35	MDA26
	AT33	DDR0_ECC4	DDR0_D27	AT35	MDA27
	AU33	DDR0_ECC5	DDR0_D28	AV37	MDA24
	AT31	DDR0_ECC6	DDR0_D29	AT35	MDA30
	AW31	DDR0_ECC7	DDR0_D30	AW35	MDA31
		DDR0_D31	DDR0_D31	AY6	MDA37
		DDR0_D32	DDR0_D32	AU6	MDA34
		DDR0_D33	DDR0_D33	AW4	MDA35
		DDR0_D34	DDR0_D34	AW6	MDA32
		DDR0_D35	DDR0_D35	AW4	MDA38
		DDR0_D36	DDR0_D36	AW6	MDA35
		DDR0_D37	DDR0_D37	AW6	MDA32
		DDR0_D38	DDR0_D38	AW4	MDA38
		DDR0_D39	DDR0_D39	AW4	MDA38
		DDR0_D40	DDR0_D40	AR1	MDA41
		DDR0_D41	DDR0_D41	AR4	MDA42
		DDR0_D42	DDR0_D42	AR4	MDA43
		DDR0_D43	DDR0_D43	AR2	MDA44
		DDR0_D44	DDR0_D44	AR2	MDA44
		DDR0_D45	DDR0_D45	AN2	MDA46
		DDR0_D46	DDR0_D46	AN1	MDA47
		DDR0_D47	DDR0_D47	AL1	MDA49
		DDR0_D48	DDR0_D48	AL4	MDA50
		DDR0_D49	DDR0_D49	AJ3	MDA50
		DDR0_D50	DDR0_D50	AJ4	MDA51
		DDR0_D51	DDR0_D51	AJ2	MDA52
		DDR0_D52	DDR0_D52	AL3	MDA44
		DDR0_D53	DDR0_D53	AJ2	MDA54
		DDR0_D54	DDR0_D54	AJ1	MDA55
	AW12	RSVD	DDR0_D55	AG1	MDA57
			DDR0_D56	AG4	MDA6
			DDR0_D57	AE3	MDA58
			DDR0_D58	AE4	MDA59
			DDR0_D59	AE3	MDA58
			DDR0_D60	AG2	MDA62
			DDR0_D61	AE1	MDA63
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HASWELL/[10SC1-F01150-11R\_10SC1-F01150-12R]

LGA1150B					
MAA80	AL19	DDR1_MA0	DDR1_D00	A634	MD80
MAA81	AK23	DDR1_MA1	DDR1_D01	A635	MD81
MAA82	AM22	DDR1_MA2	DDR1_D02	AG35	MD82
MAA83	AM23	DDR1_MA3	DDR1_D03	AH35	MD83
MAA84	AF23	DDR1_MA4	DDR1_D04	AD34	MD84
MAA85	AL24	DDR1_MA5	DDR1_D05	AD35	MD85
MAA86	AY24	DDR1_MA6	DDR1_D06	AG34	MD86
MAA87	AV25	DDR1_MA7	DDR1_D07	AH34	MD87
MAA88	AU26	DDR1_MA8	DDR1_D08	AL34	MD88
MAA89	AW25	DDR1_MA9	DDR1_D09	AL35	MD89
MAA90	AF18	DDR1_MA10	DDR1_D010	AK31	MD10
MAA91	AV15	DDR1_MA11	DDR1_D011	AL31	MD11
MAA92	AM11	DDR1_MA12	DDR1_D012	AK35	MD12
MAA93	AY15	DDR1_MA13	DDR1_D013	AK32	MD13
MAA94	AV27	DDR1_MA14	DDR1_D014	AL32	MD14
MAA95	AU28	DDR1_MA15	DDR1_D015	AK34	MD15
MODT_B0	AM17	DDR1_ODT0	DDR1_D016	AN34	MD16
MODT_B1	AL16	DDR1_ODT1	DDR1_D017	AP31	MD17
MODT_B2	AM16	DDR1_ODT2	DDR1_D018	AP31	MD18
MODT_B3	AK15	DDR1_ODT3	DDR1_D019	AP35	MD19
			DDR1_D020	AN35	MD20
			DDR1_D021	AN32	MD21
			DDR1_D022	AP32	MD22
	AM26	DDR1_EC00	DDR1_D023	AP32	MD23
	AM25	DDR1_EC01	DDR1_D024	AM29	MD24
	AF25	DDR1_EC02	DDR1_D025	AR28	MD25
	AF26	DDR1_EC03	DDR1_D026	AR28	MD26
	AF26	DDR1_EC04	DDR1_D027	AR28	MD27
	AR26	DDR1_EC05	DDR1_D028	AR28	MD28
	AR26	DDR1_EC06	DDR1_D029	AL28	MD29
	AR25	DDR1_EC07	DDR1_D030	AP29	MD30
			DDR1_D031	AP28	MD31
			DDR1_D032	AP12	MD32
			DDR1_D033	AL13	MD33
			DDR1_D034	AL12	MD34
			DDR1_D035	AR13	MD35
			DDR1_D036	AP13	MD36
			DDR1_D037	AM13	MD37
			DDR1_D038	AM13	MD38
			DDR1_D039	AM12	MD39
			DDR1_D040	AR9	MD40
			DDR1_D041	AP9	MD41
			DDR1_D042	AR6	MD42
			DDR1_D043	AP6	MD43
			DDR1_D044	AR10	MD44
			DDR1_D045	AR7	MD45
			DDR1_D046	AP7	MD46
			DDR1_D047	AM9	MD47
			DDR1_D048	AL9	MD48
			DDR1_D049	AL6	MD49
			DDR1_D050	AL7	MD50
			DDR1_D051	AM10	MD51
			DDR1_D052	AL10	MD52
			DDR1_D053	AL10	MD53
			DDR1_D054	AM7	MD54
			DDR1_D055	AH6	MD55
			DDR1_D056	AH6	MD56
			DDR1_D057	AH6	MD57
			DDR1_D058	A67	MD58
			DDR1_D059	A67	MD59
			DDR1_D060	A67	MD60
			DDR1_D061	AF7	MD61
			DDR1_D062	AF7	MD62
			DDR1_D063	AF7	MD63
			DDR1_D064	AF35	MD64
			DDR1_D065	AL33	MD65
			DDR1_D066	AP33	MD66
			DDR1_D067	AN28	MD67
			DDR1_D068	AN12	MD68
			DDR1_D069	AP8	MD69
			DDR1_D070	AL8	MD70
			DDR1_D071	AG7	MD71
			DDR1_D072	AN25	MD72
			DDR1_D073	AF34	MD73
			DDR1_D074	AK33	MD74
			DDR1_D075	AN29	MD75
			DDR1_D076	AN13	MD76
			DDR1_D077	AR8	MD77
			DDR1_D078	AM8	MD78
			DDR1_D079	AG6	MD79
			DDR1_D080	AN26	MD80
			DDR1_D081		
			DDR1_D082		
			DDR1_D083		
			DDR1_D084		
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			DDR1_D320		
			DDR1_D321		
			DDR1_D322		
			DDR1_D323		
			DDR1_D324		

HASWELL/10SC1-F01150-11R\_10SC1-F01150-12R

LGA1150  
ILM\_BP/1156/CSP/[12KRC-0F0001-52R\_12KRC-0F0001-51R]



DDR BUS

7	MODT_A[0..3]	↔	MODT_A[0..3]
8	MODT_B[0..3]	↔	MODT_B[0..3]
7	MDA[0..63]	↔	MDA[0..63]
8	MDB[0..63]	↔	MDB[0..63]
7	DQSA[0..7]	↔	DQSA[0..7]
7	-DQSA[0..7]	↔	-DQSA[0..7]
7	MAAA[0..15]	↔	MAAA[0..15]
8	MAAB[0..15]	↔	MAAB[0..15]
8	DQSB[0..7]	↔	DQSB[0..7]
8	-DQSB[0..7]	↔	-DQSB[0..7]

<b>Gigabyte Technology</b>				
Title				
<b>CPU LGA1150-B</b>				
Size	Document Number			Rev
Custom	<b>GA-P85-D3</b>			<b>2.0</b>
Date:	Wednesday, February 26, 2014		Sheet	5 of 33

**(F, J)**

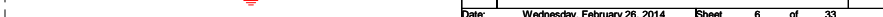


**(G,H,I)**

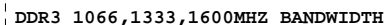


(X30)

(X15)



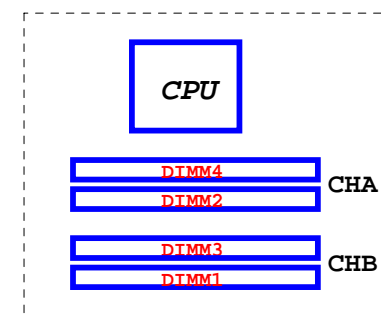
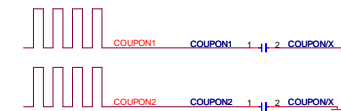




```
DDR3 1066MHZ
DDR3 clock=533MHZ
DDR3 single channel bandwidth=533x2x8Byte=8.5GB/s
DDR3 dual channel bandwidth=533x2x2x8Byte=17GB/s
```

```
DDR3 1333MHZ
DDR3 clock=667MHZ
DDR3 single channel bandwidth=10.6GB/s
DDR3 dual channel bandwidth=21GB/s
```

```
| DDR3 1600MHZ
| DDR3 clock=800MHZ
| DDR3 single channel bandwidth=12.8GB/s
| DDR3 dual channel bandwidth=25.6GB/s
```



DMI:12/4/4/4/12(breakout min 8/4/4/4/8)  
Impedance=85 +- 17.5%

DMI:12/4/4/4/12(breakout min 8/4/4/4/8)  
Impedance=85 +- 17.5%



28 PCH\_USB3\_RXN0



**\_\_\_\_\_**

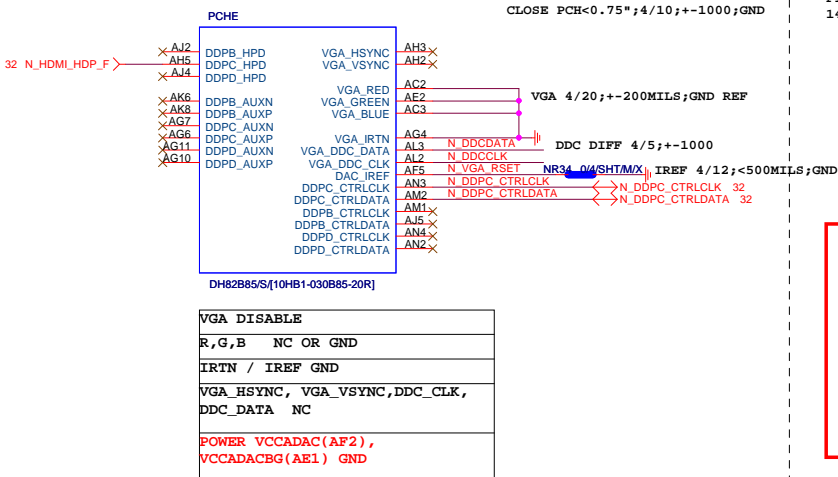


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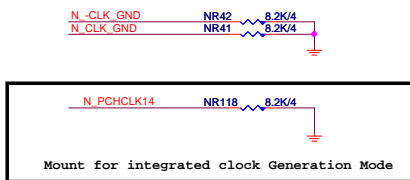
# PCH (E)



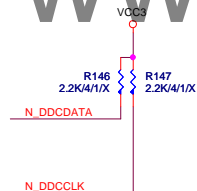
# PCH (G)



## PCH CLK PD



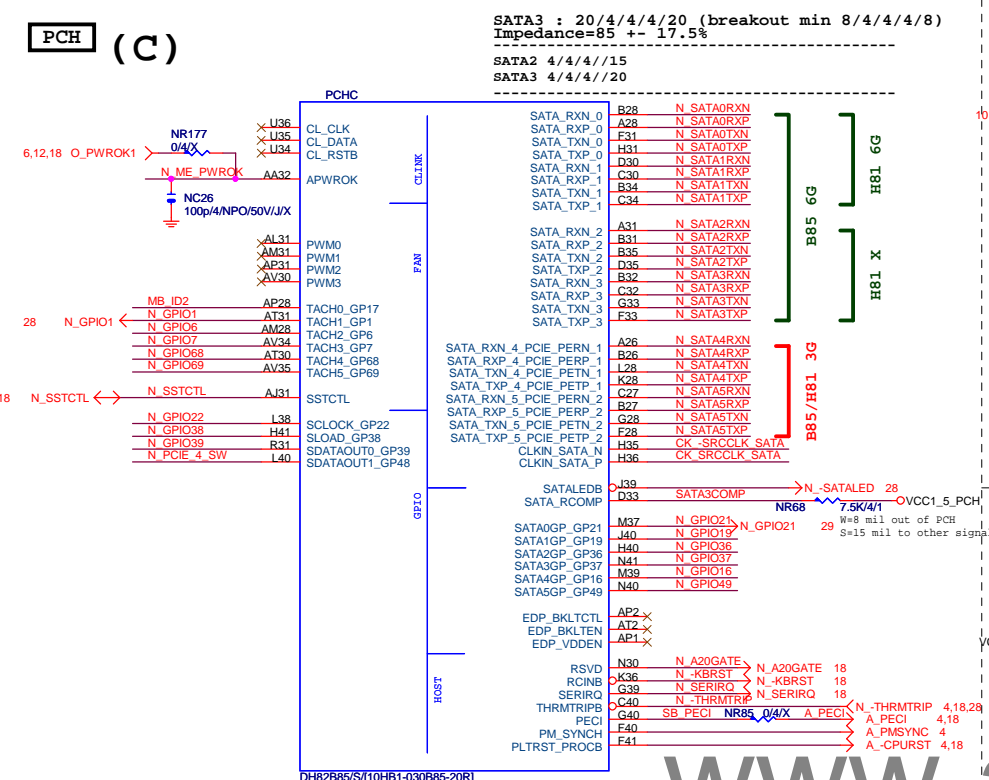
## VGA DDC



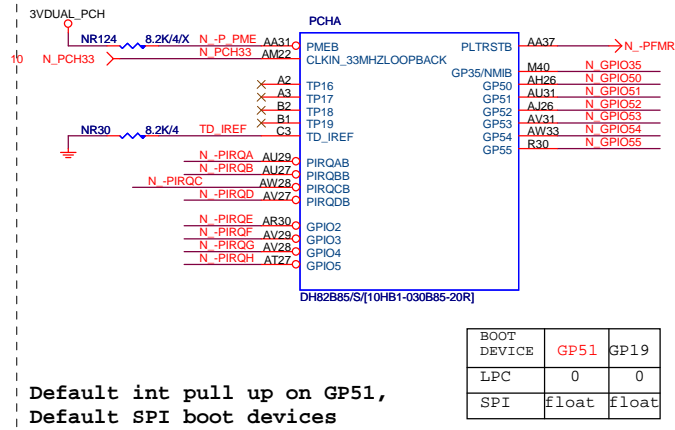
## VGA DDC

## VGA CONNECTOR

PCH (C)



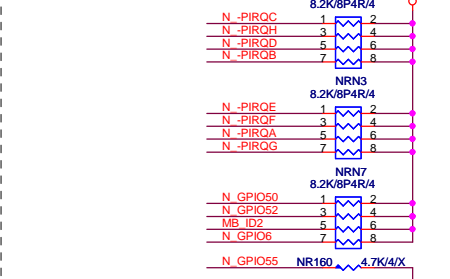
PCH (A)



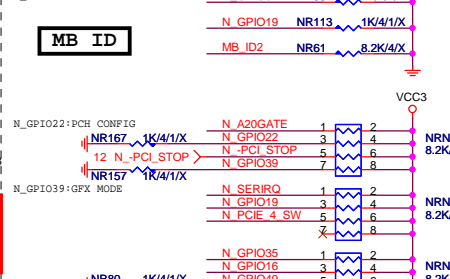
PCH CLK PD



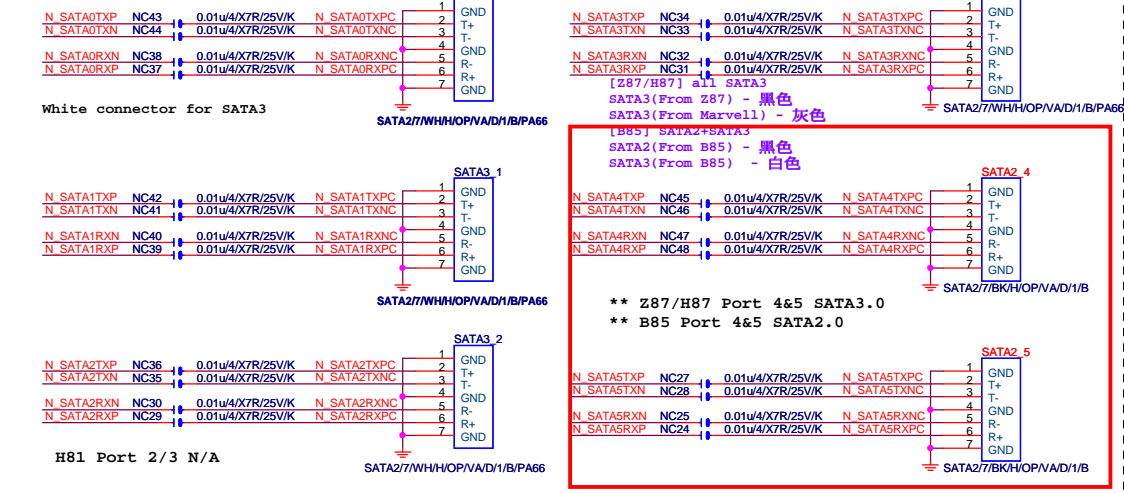
PCH PU/PD



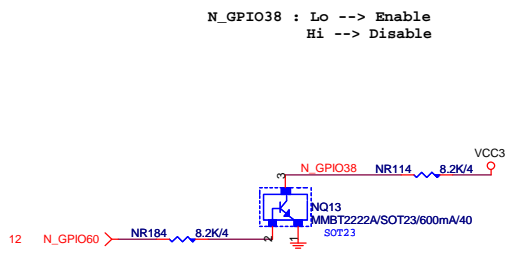
MB ID



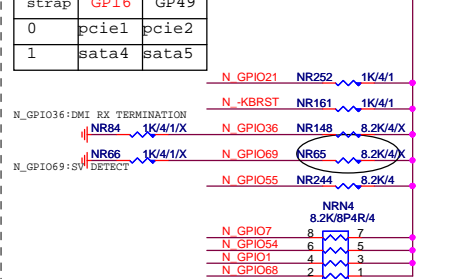
SATA CONNECTOR



GPIO38 Ctrl



GPIO38 Ctrl



Gigabyte Technology

PCH HOST , SATA, PCI		
Size	Document Number	Rev
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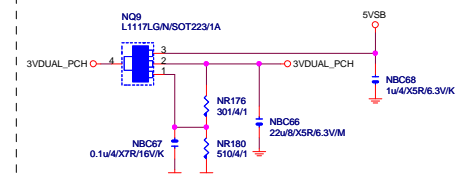
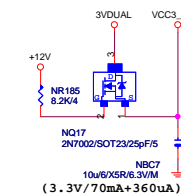




**PCH (I)**



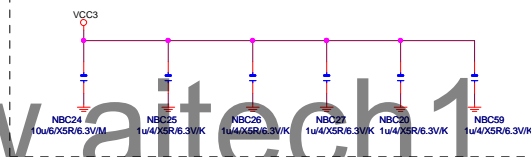
## 3VDUAL\_PCH



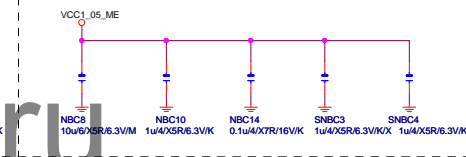
The diagrams show two components, NR5 and NRN1, connected to VCC3 and VCC1\_05\_PCH pins. The NR5 component is connected to VCC3\_ME and VCC3. The NRN1 component is connected to VCC1\_05\_ME and VCC1\_05\_PCH. Both components have pins 1, 2, 3, 4, 5, 6, 7, and 8. The connections are as follows:

- NR5: Pin 1 to VCC3\_ME, Pin 2 to VCC3, Pin 3 to GND, Pin 4 to GND, Pin 5 to GND, Pin 6 to GND, Pin 7 to GND, Pin 8 to GND.
- NRN1: Pin 1 to VCC1\_05\_ME, Pin 2 to VCC1\_05\_PCH, Pin 3 to GND, Pin 4 to GND, Pin 5 to GND, Pin 6 to GND, Pin 7 to GND, Pin 8 to GND.

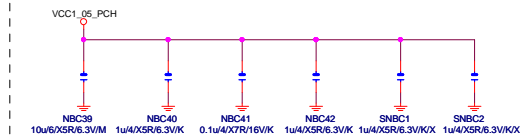
( 3.3V ) ( X6 )



(1.05V) (x5)



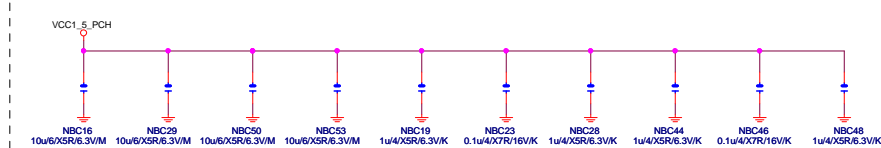
(1.05V) (X6)



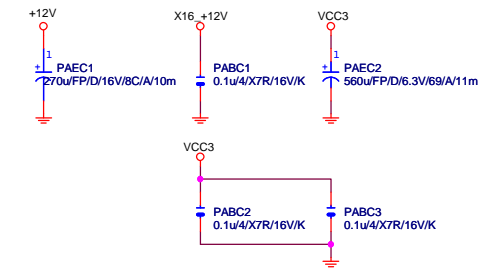
(1.05V)(x2) (3.3V) (x2)



(1.5V) (x10)

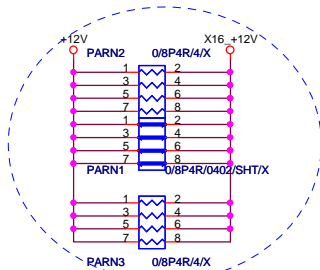


## PCIEX16 CAP



## PCIEX16 PROTECT SHT

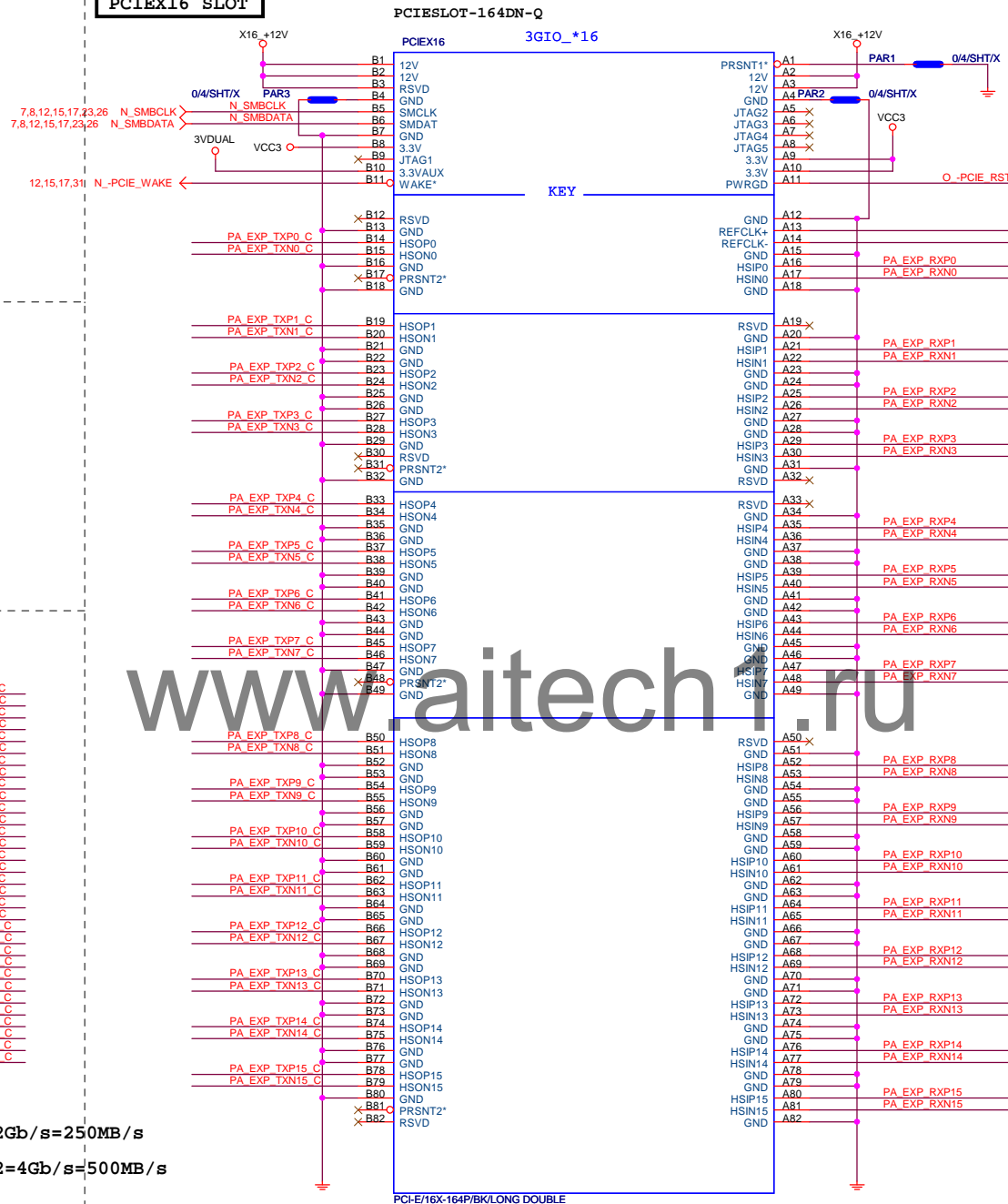
+12 protect  
short-wire test



## PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

## PCIEX16 SLOT



The auxiliary reset circuit is only required for PCIe Gen3 margining and functional link training

PCIEX16:16/5/5/5/16

PA EXP RXP0..15]	>>>PA_EXP_RXP[0..15]	4
PA EXP RXN0..15]	>>>PA_EXP_RXN[0..15]	4
PA EXP TXP0..15]	>>>PA_EXP_TXP[0..15]	4
PA EXP TXN0..15]	>>>PA_EXP_TXN[0..15]	4

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWITH=2.5GHz\*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHz\*(8b/10b)X2=4Gb/s=500MB/s

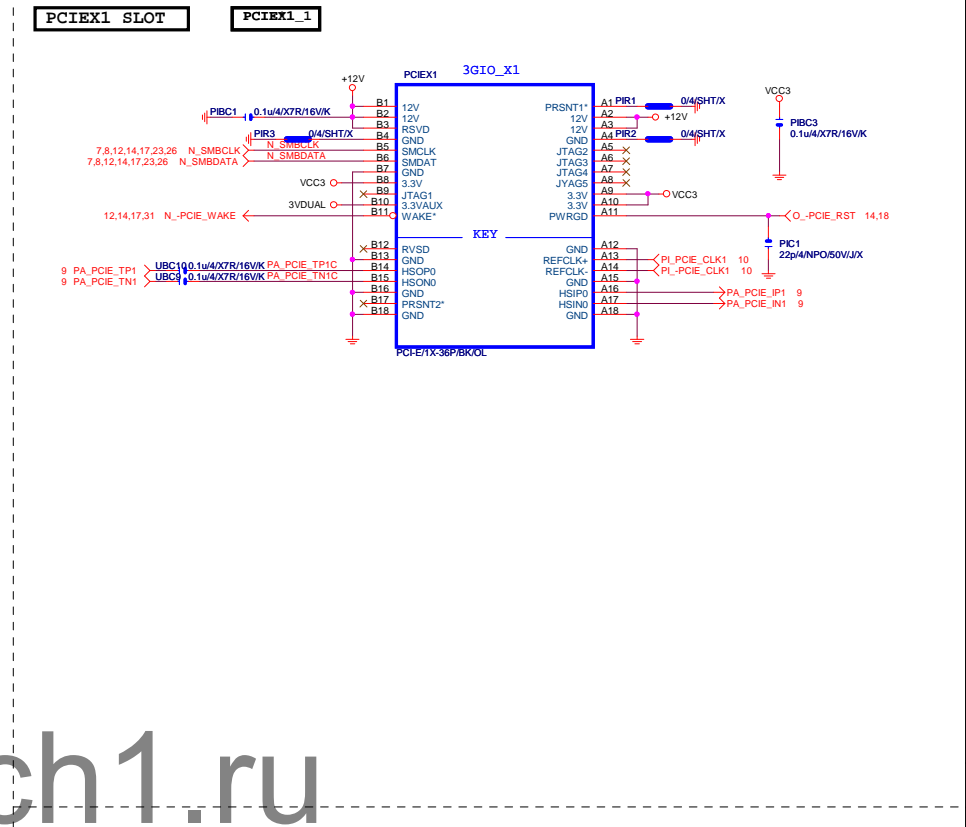
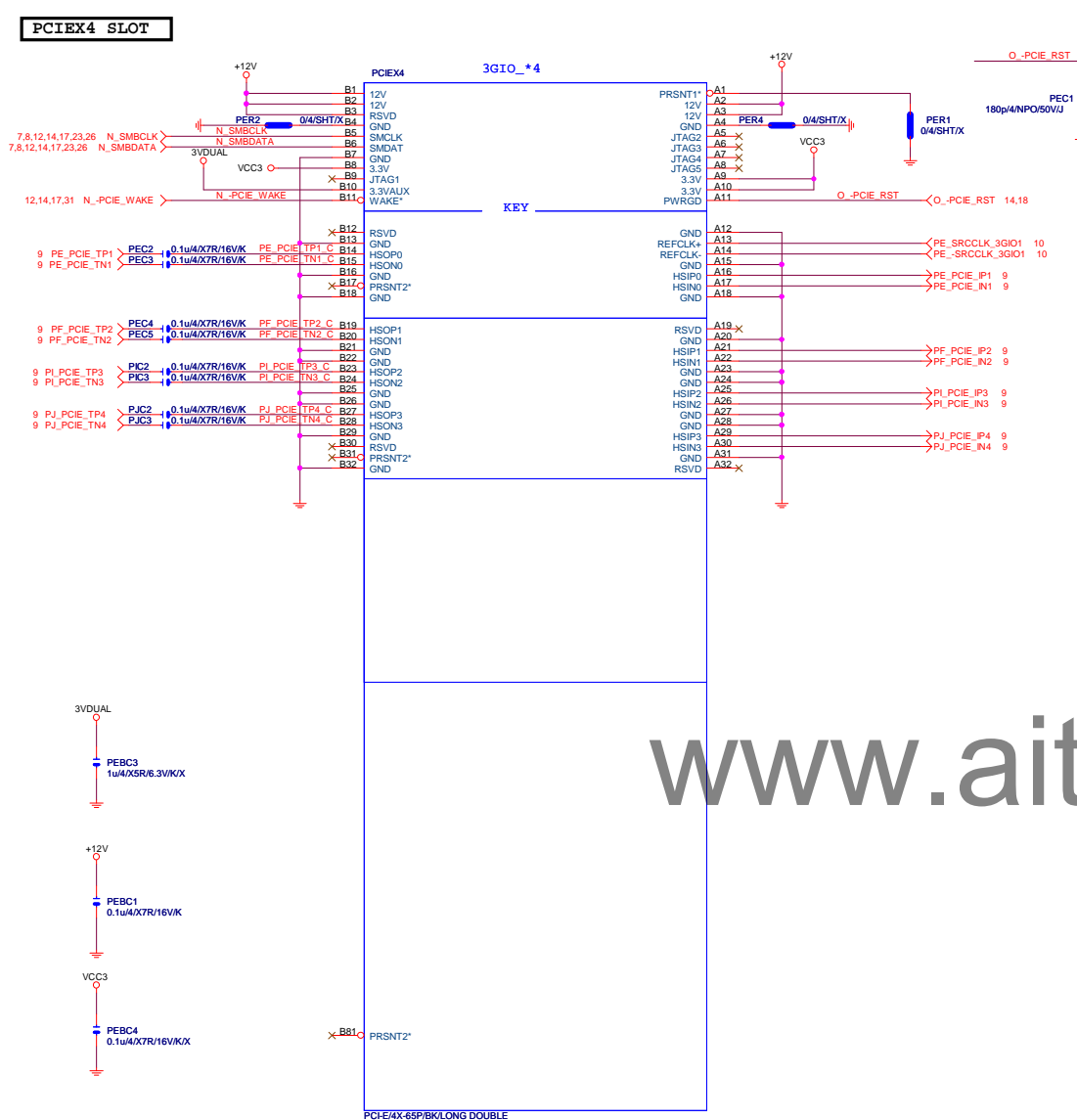
PCE-E X16(單向) BANDWITH=2.5GHz\*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHz\*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ

Gigabyte Technology

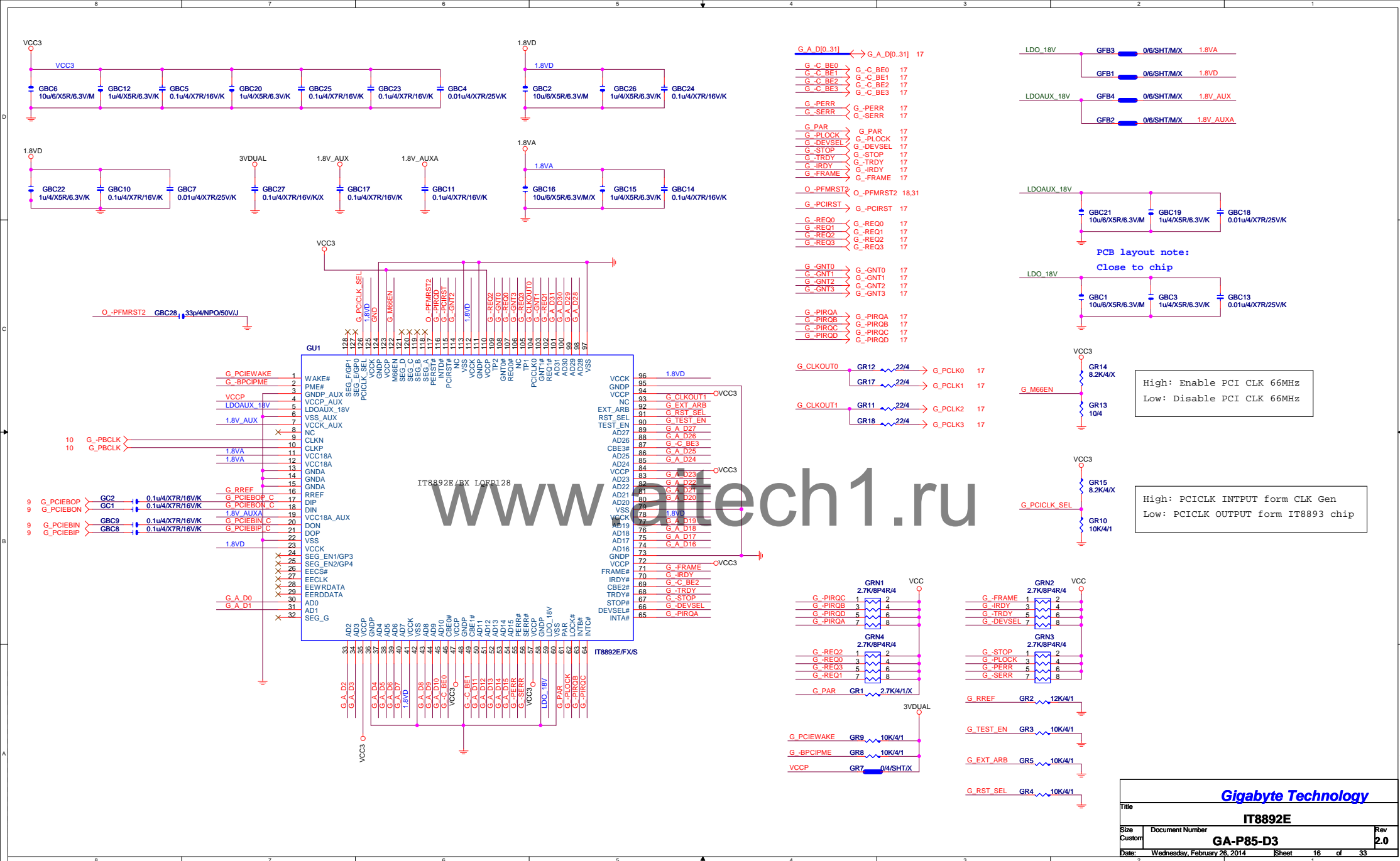
PCI EXPRESS * 16		
Size	Document Number	Rev
Custom	GA-P85-D3	2.0
Date:	Wednesday, February 26, 2014	Sheet 14 of 33



PCIEX4/X1 SWITCH

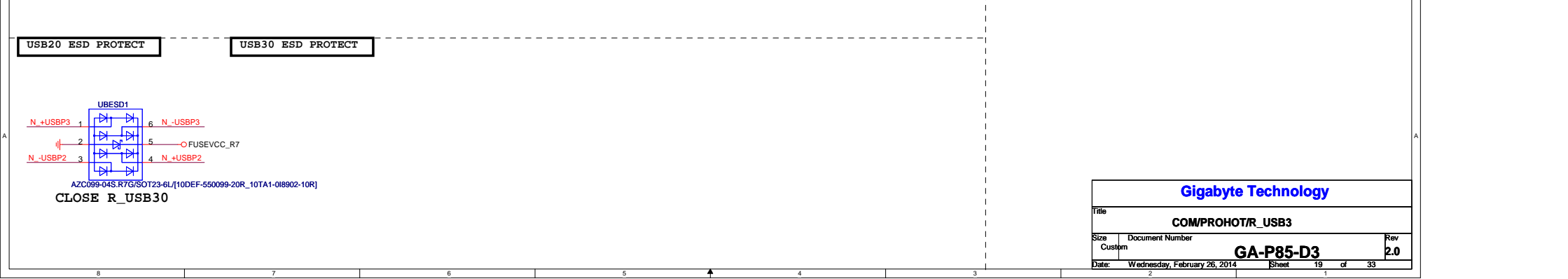
	N_PCIE_4_SW (PCH_GPIO48)	PCIEX4_X1 (SIO_GPIO26)
PCIEX4 No devices	H	H
PCIEX4 --> X1	H	H
PCIEX4 Have devices	L	L
PCIEX4 --> X4	L	L
PCIEX1_1/2 --> N/A		

Function	SEL
xI--> x0a	L;PCIEX4 SLOT-->X1
xI--> x0b	H;PCIEX4 SLOT-->X4



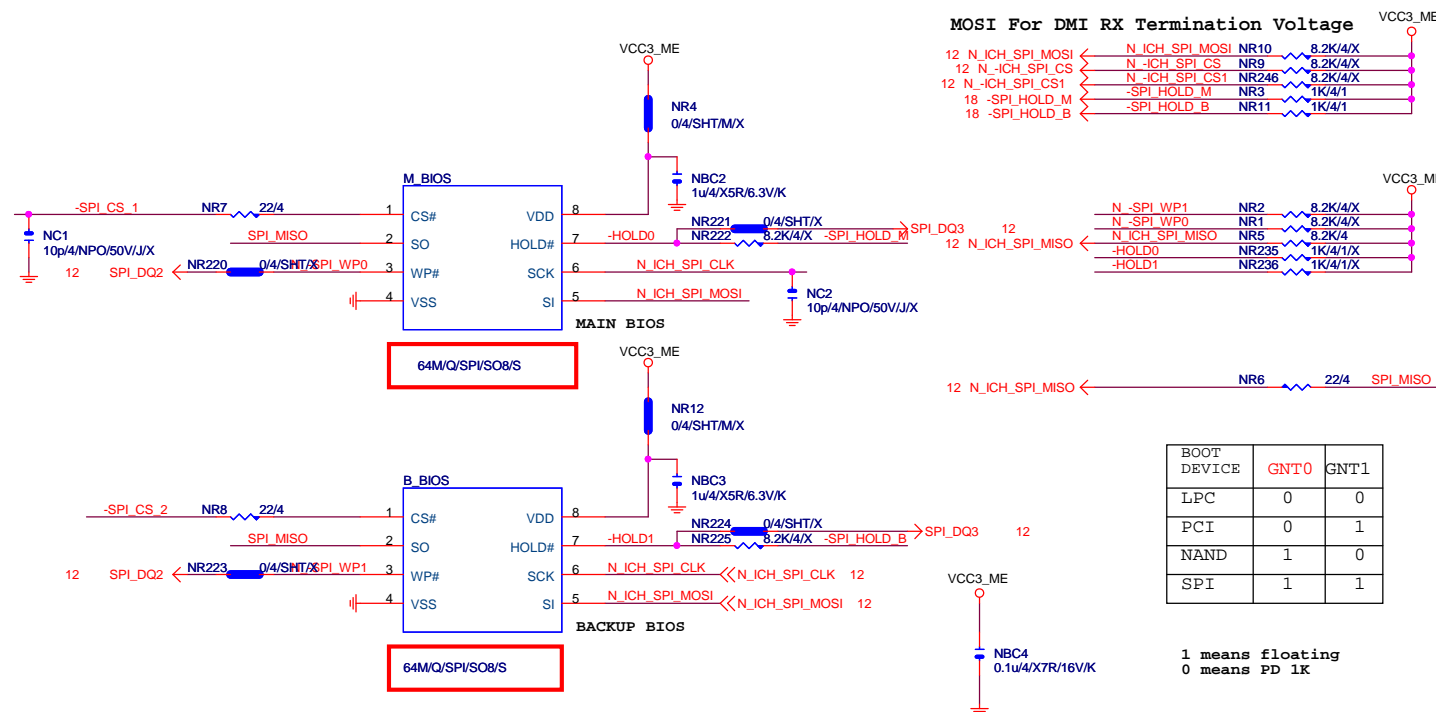
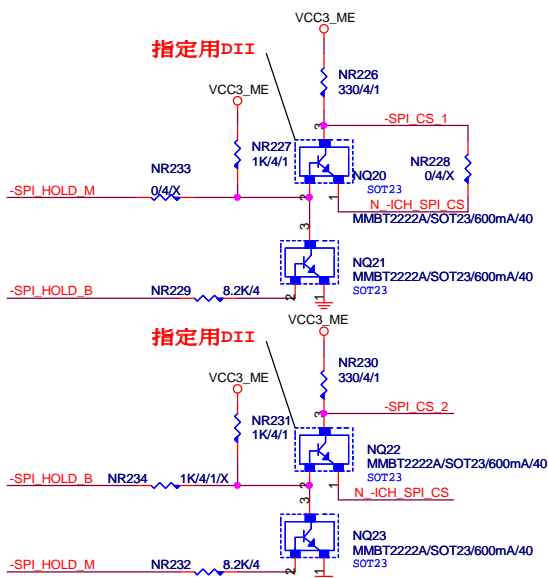








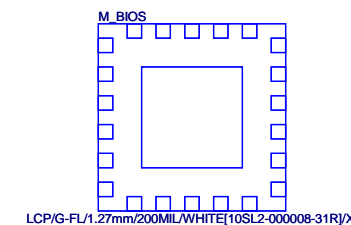
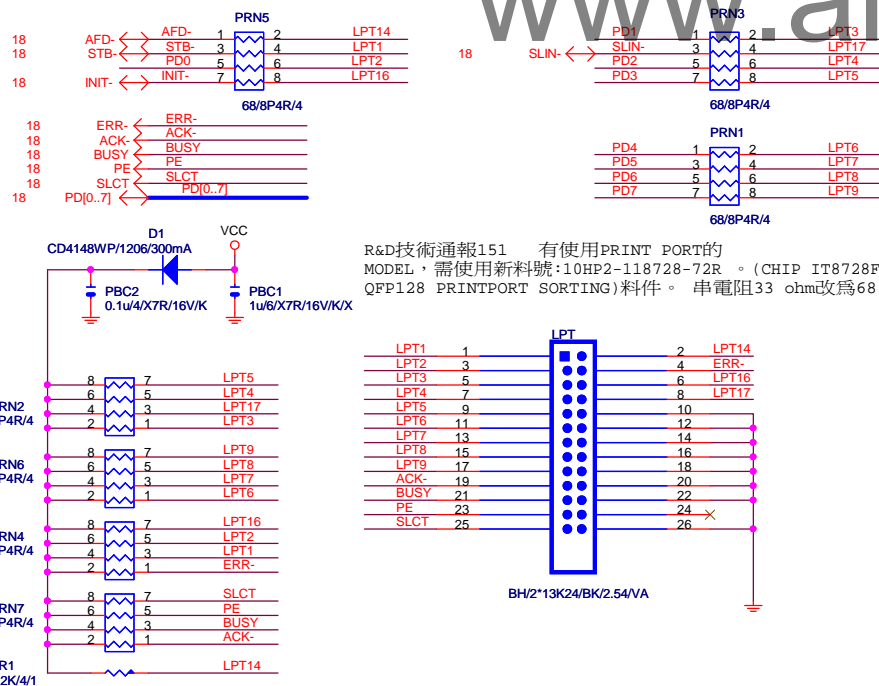
## DUAL BIOS



BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating  
0 means PD 1K

## LPT PORT



R&D技術通報151 有使用PRINT PORT的  
MODEL, 需使用新料號:10HP2-118728-72R。(CHIP IT8728F/EX (GB) ITE/SMD  
QFP128 PRINTPORT SORTING)料件。串電阻33 ohm改為68 ohm。

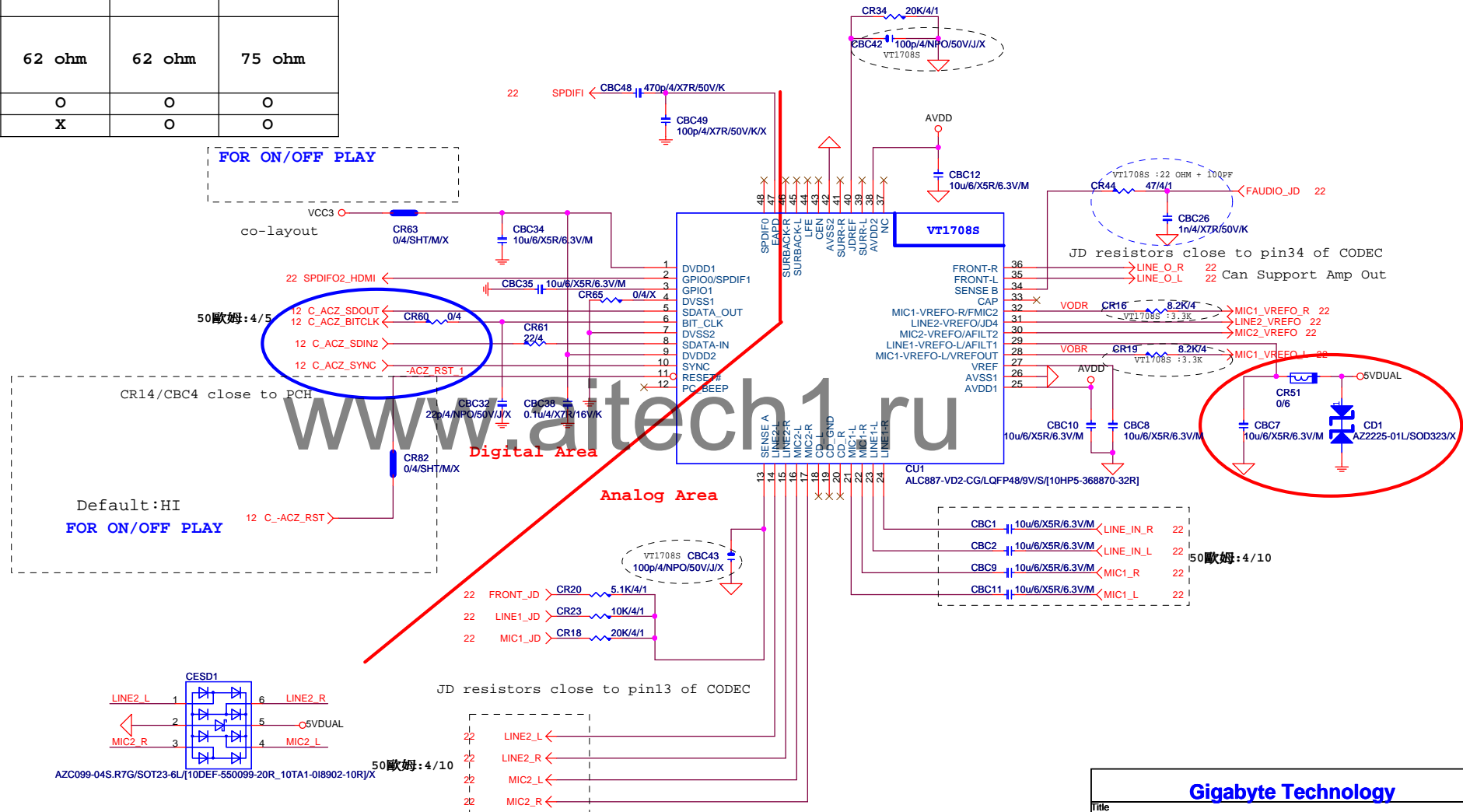
www.aitech1.ru

**Gigabyte Technology**

Title		<b>BIOS</b>	
Size Custom	Document Number	<b>GA-P85-D3</b>	Rev <b>2.0</b>
Date:	Wednesday, February 26, 2014	Sheet	20 of 33

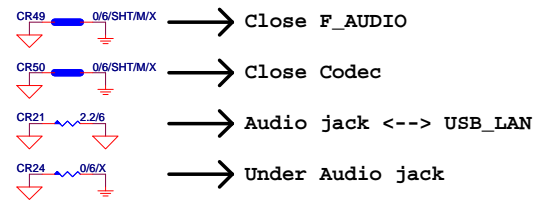


	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR16/CR19 CR52/CR56/CR10/CR9	8.2K/4	8.2K/4	3.3K/4/1
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	X	O	O

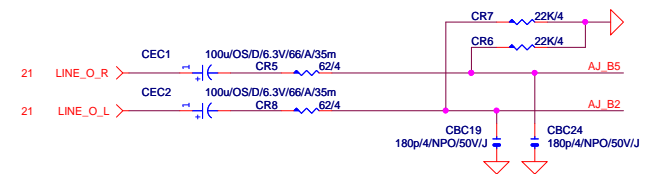


Gigabyte Technology

Title	HD AUDIO ALC887	
Size	Document Number	GA-P85-D3
Custom		Rev 2.0
Date:	Wednesday, February 26, 2014	Sheet 21 of 33



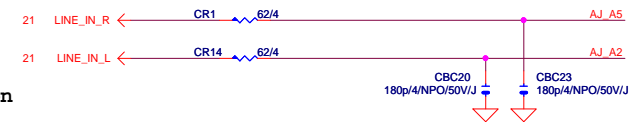
#### LINE-OUT



#### LINE-IN

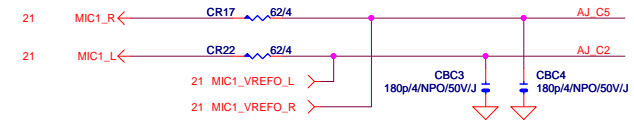
Verify MIC function  
in LINE-in

Only reserved for ALC888



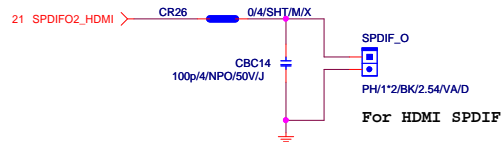
For 889A/888

#### MIC-IN

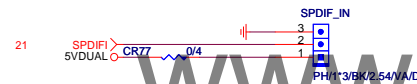


#### SURROUND

#### SPDIF\_OUT



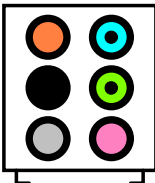
#### SPDIF\_IN



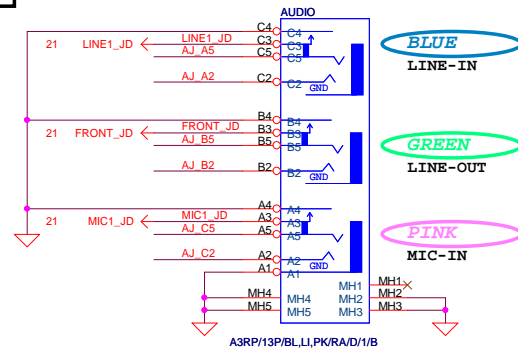
#### CEN/LFE

#### SURR BACK

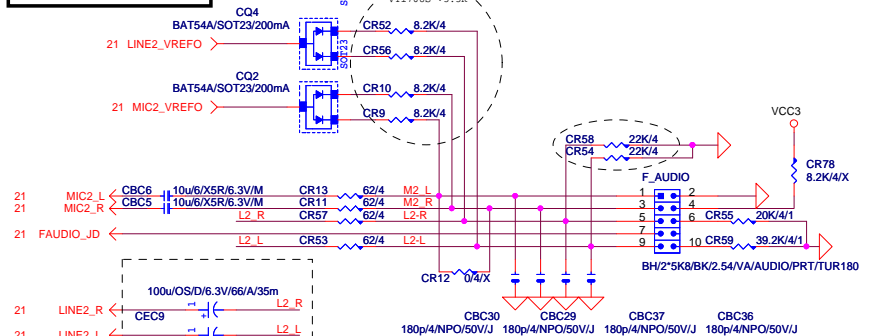
#### AZALIA JACK



#### AZALIA JACK



#### AZALIA FRONT PANEL



Gigabyte Technology

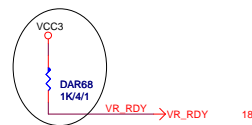
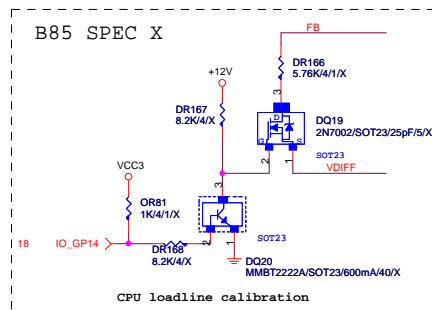
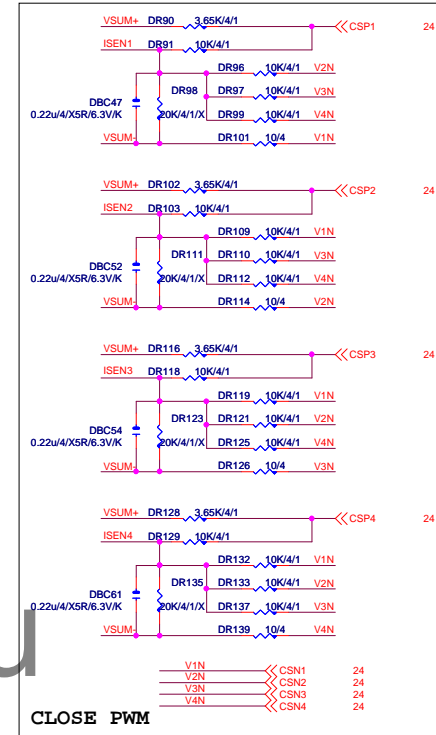
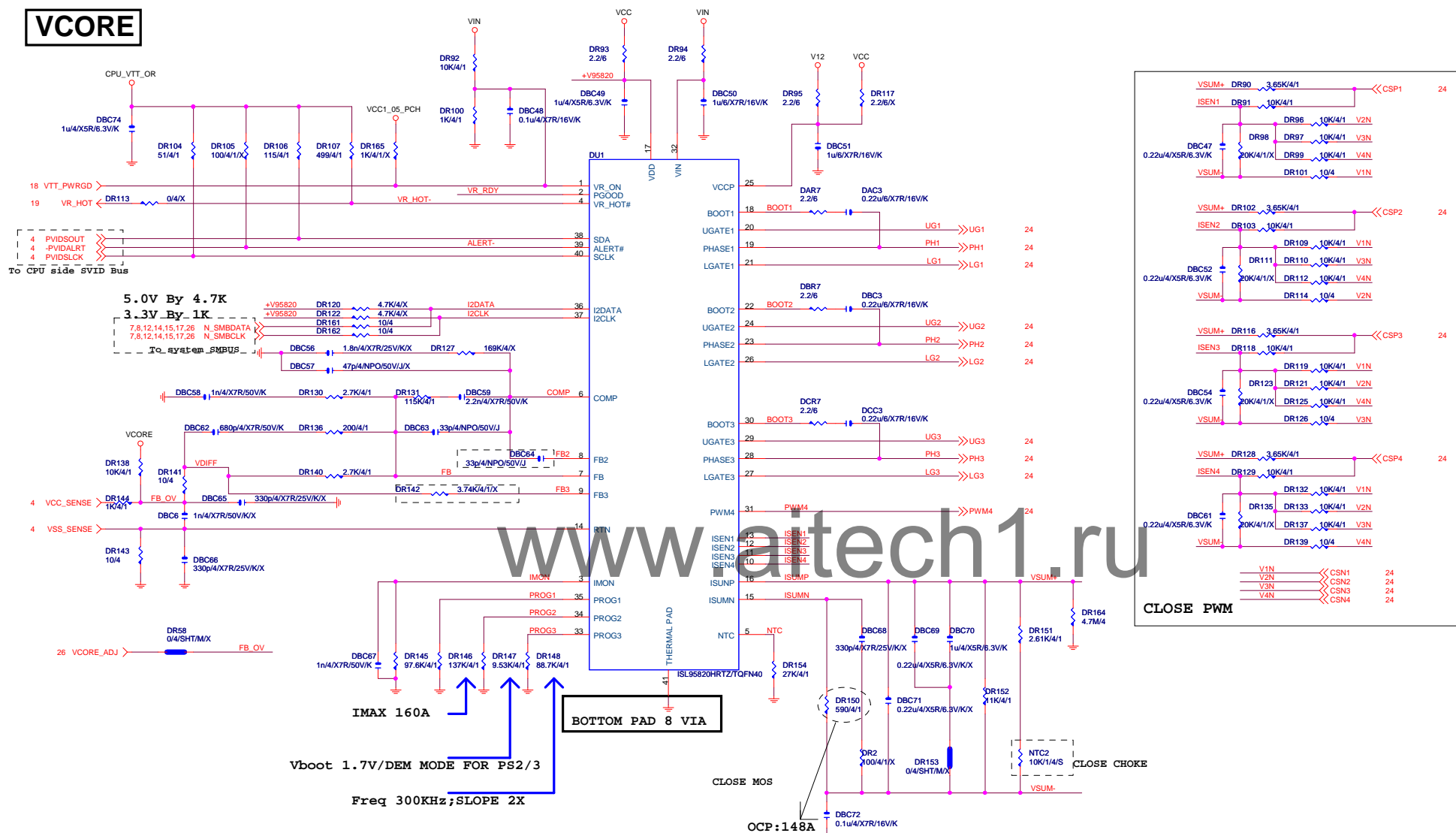
AUDIO JACK

GA-P85-D3

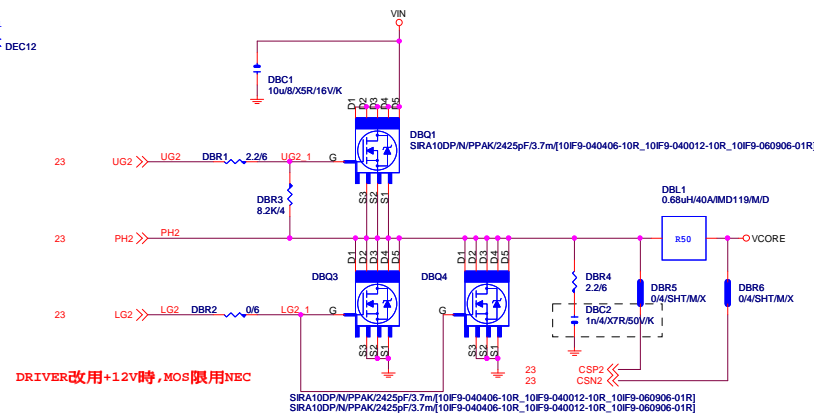
Rev 2.0

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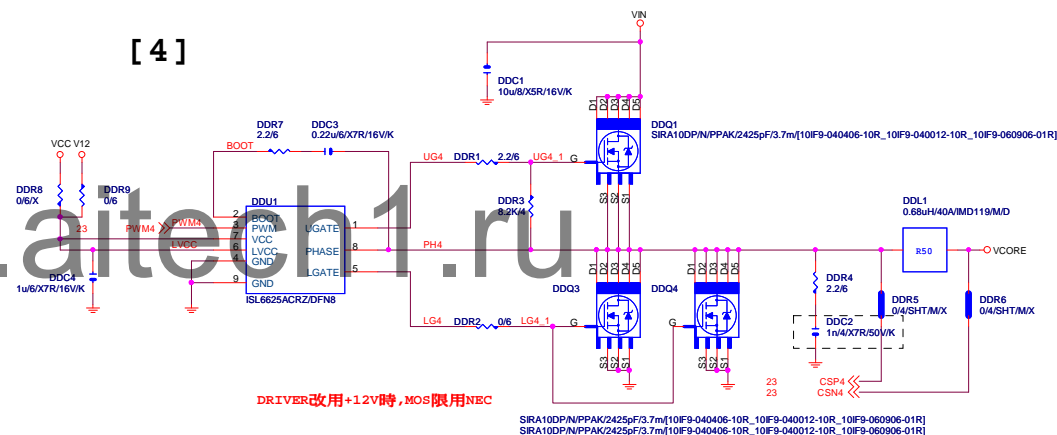
## VCORE



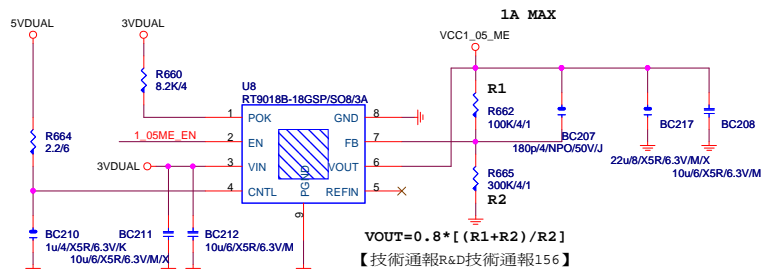
[1]



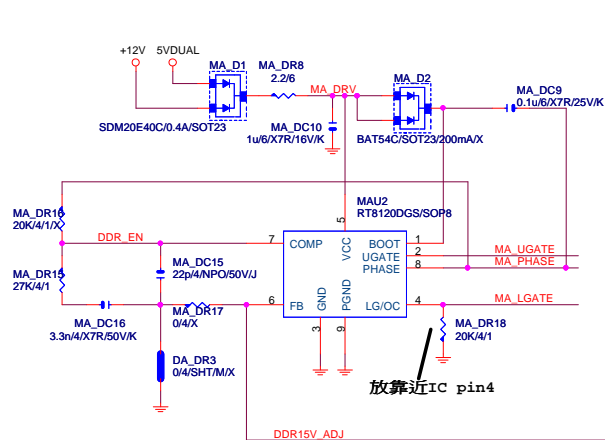
## [3]



## VCC1\_05\_ME



**DDR\_15V**



放靠近IC pin4

DDR\_EN < DDR\_EN\_CON 18

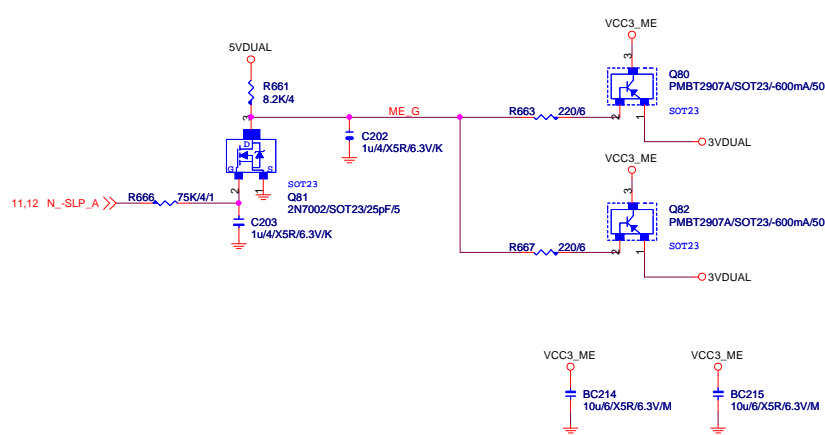
PWR SEQ

VIN=5V,VOUT=1.5V,IOUT=25A,PHASE=1  
IRMS=11.45A

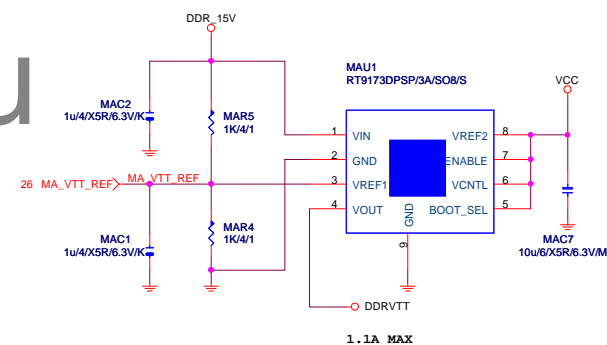
560uF/P/D/6.3V/68/8m RIPPLE CURRENT=4.7A  
Coefficient=1.7(85°C),1(105°C)  
VIN Ripple current=4.7X1.7=7.99A(85°C)  
-->故固態電容須2X7.99=15.98>11.45A

```
OCP:35.82A for Rds=6.7m for vishay@4.5V
OCP:72.727A for Rds=3.3m for renesas@10V
OCP:48A=Roset*Iocset / Rds(on)
      =12K*10uA / [5/5]
```

## VCC3\_ME



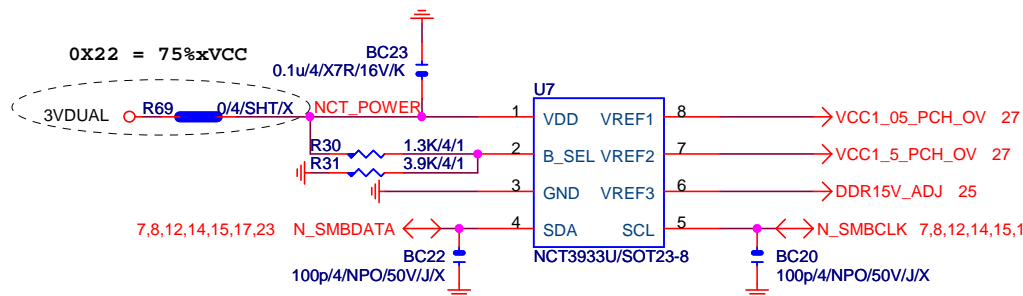
DDRVTT



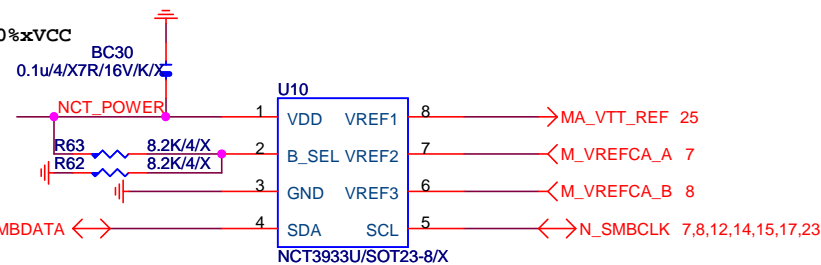
Remote sense 請從最重的負載端點拉回

$$\begin{aligned} 0.8 \cdot (1 + R_S/R_O) &= V_{out} \\ 0.8 \cdot [1 + 2K/2.2K] &= \\ 1.527V \end{aligned}$$

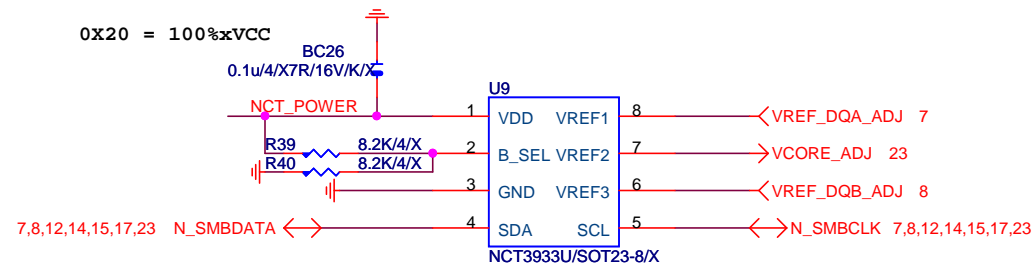
# OVER VOLTAGE



0X2A = 0%xVCC



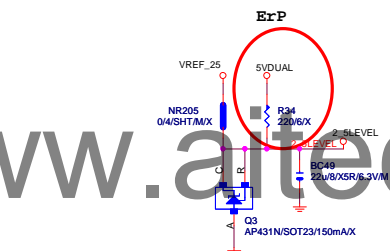
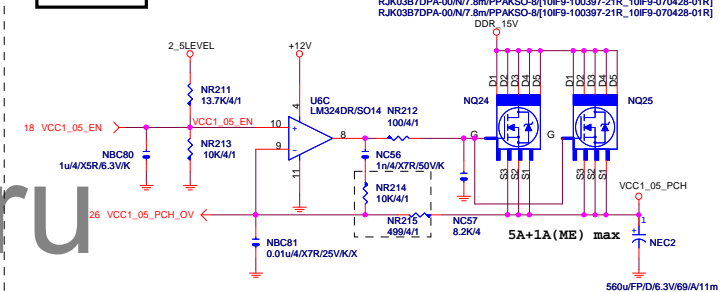
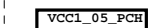
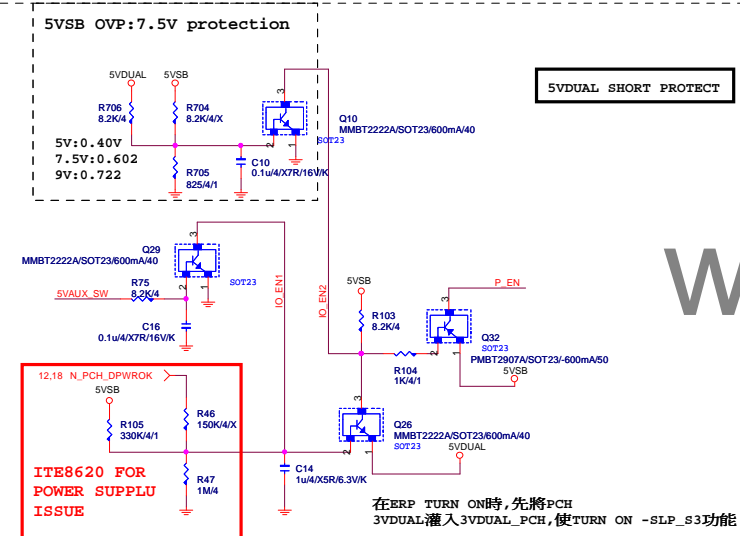
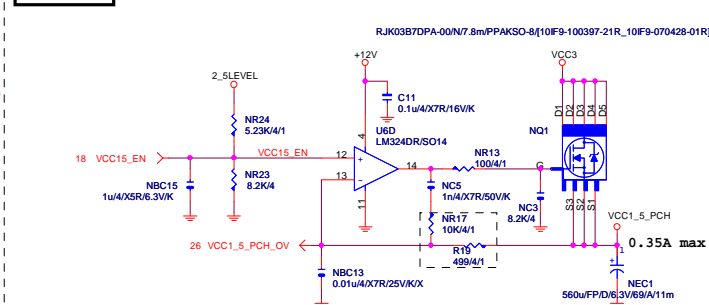
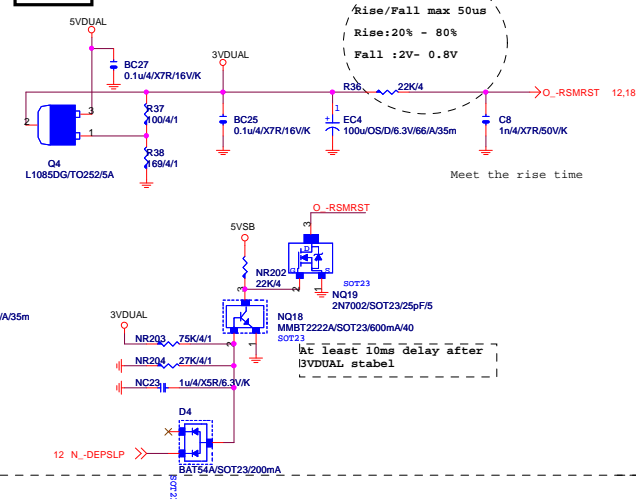
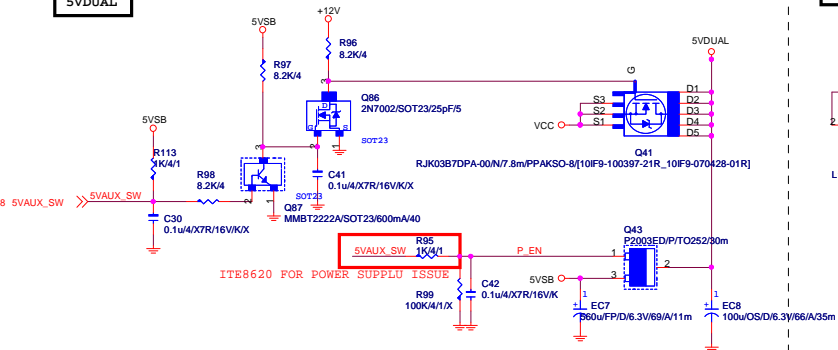
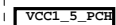
0X20 = 100%xVCC



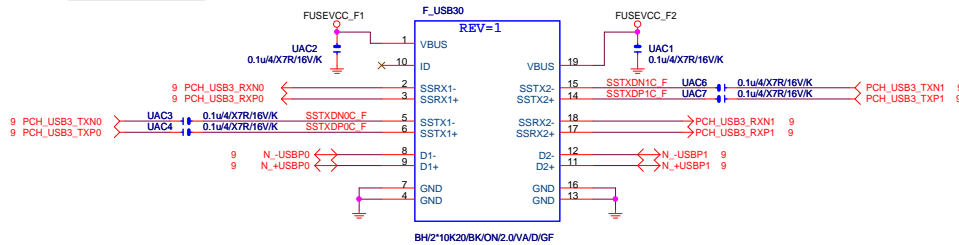
NCT3933	0X2A	0X20	0X22
VREF1	DDRVTT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

**Gigabyte Technology**

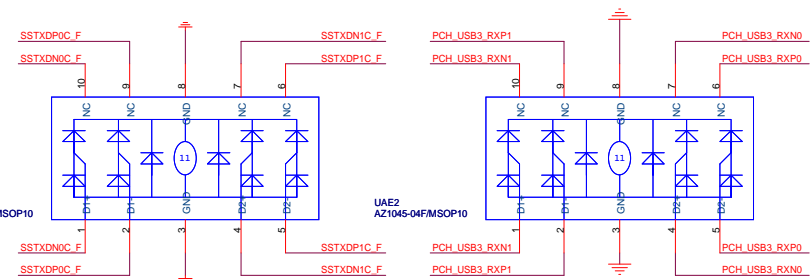
Title		
CPU CORE VR-2		
Size	Document Number	Rev
Custom	GA-P85-D3	2.0
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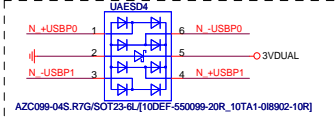
# Front USB3.0



# F\_USB30 ESD PROTECT

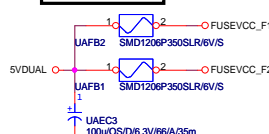


# BLUE

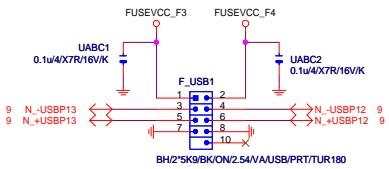


Close to connector

# F\_USB30 PWR

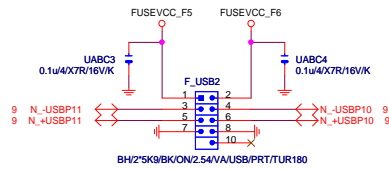


# FRONT USB1



Close to connector

# FRONT USB2

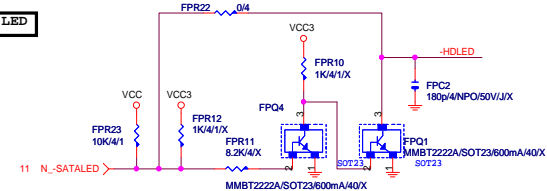


Close to connector

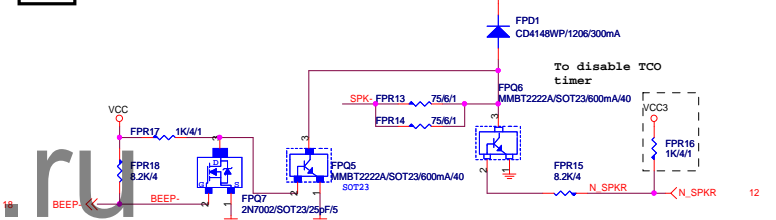
# FRONT USB3



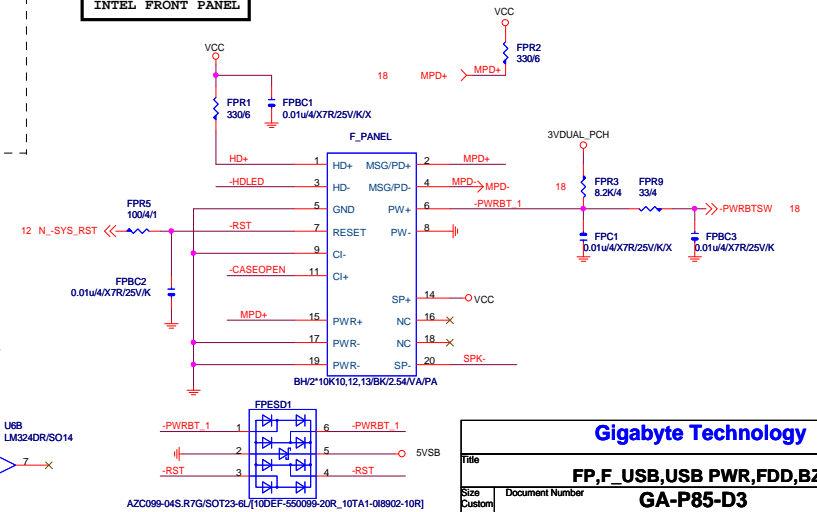
# SATA LED



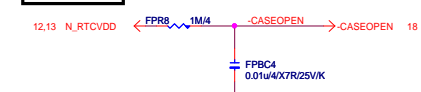
# SPKR



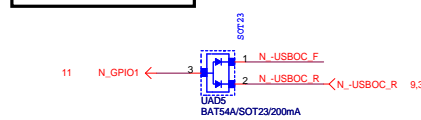
# INTEL FRONT PANEL



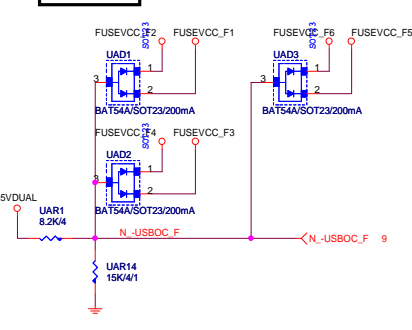
# CASE OPEN



# F\_USB POWER PROTECT

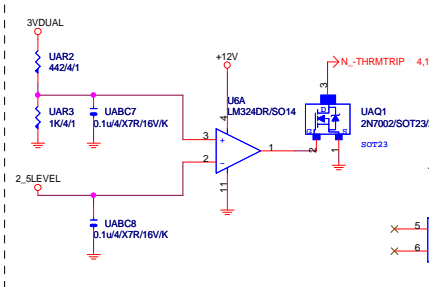


# -USBOC\_F



# USB2.0 Signal & power short protection

USB2.0 Signal > 4.85V  
Enable --> 3VUUAL=3.75V



Gigabyte Technology			
FP,F_USB,USB PWR,FDD,BZ			
GA-P85-D3			
Rev	2.0		
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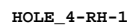
Patch some PSU no internal  
pull up resistor



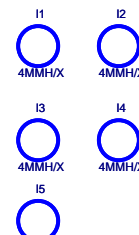
```

To prevent the 5VSB
under loading when
- boot - - - - -

```



K1-ICT



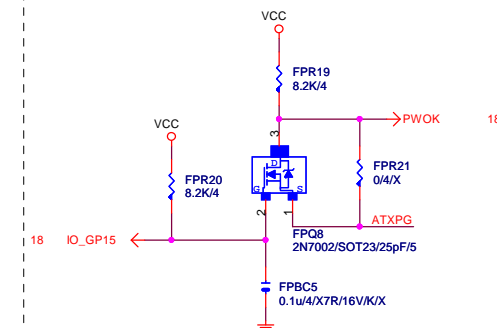
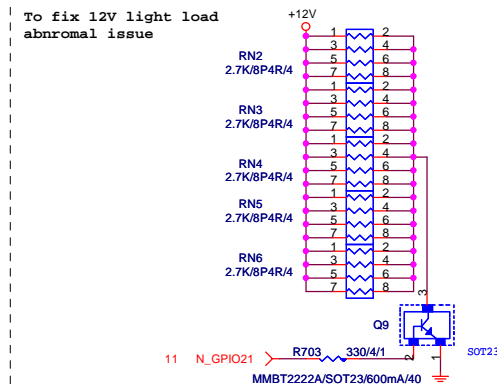
4MMH

### CPU Frequency Selection

FSLB	FSLA	CPU
0	0	100M <Default>
0	1	133M
1	0	200M
1	1	166M

## PWOK PATCH

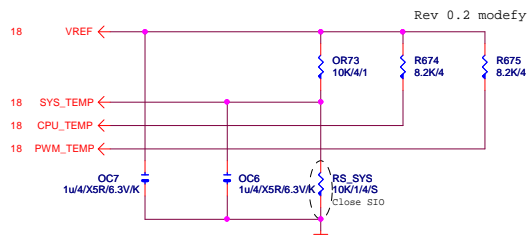
【技術通報R&amp;D技術通報154】



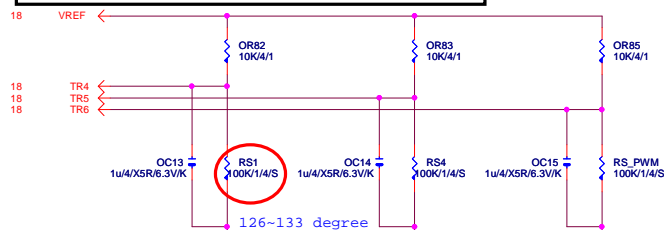
## Gigabyte Technology

Title			
<b>ATX POWER CONNECTOR</b>			
Size Custom	Document Number	<b>GA-P85-D3</b>	Rev <b>2.0</b>
Date:	Wednesday, February 26, 2014	Sheet	29 of 33

# TEMP H/W MONITOR

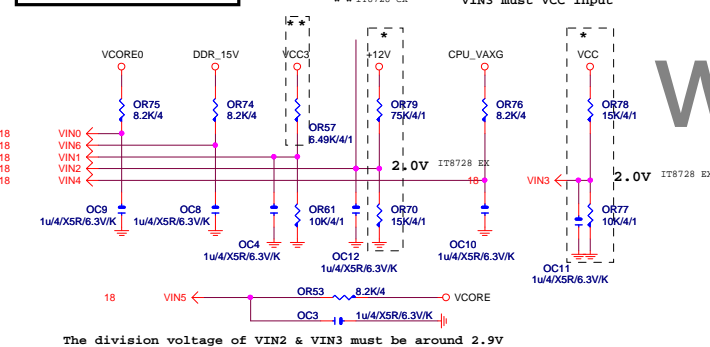


# -PROCHOT:有mos heatsink不用prochot function

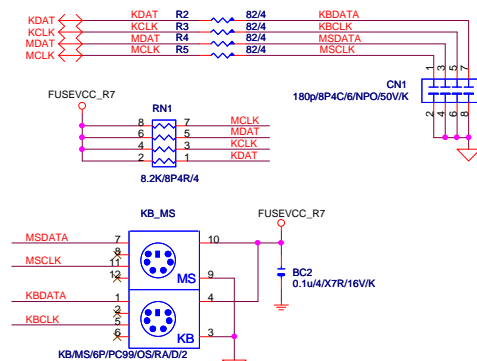


RS1、RS2、RS3 CLOSE CPU VR MOSFET

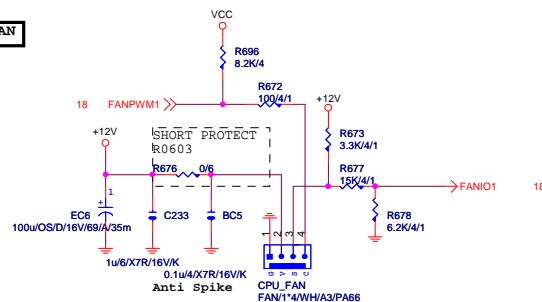
# VOLTAGE-- H/W MONITOR



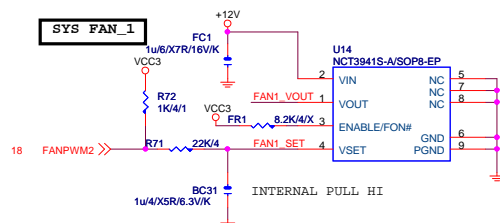
# KB/USB



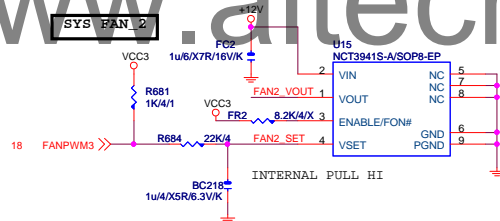
# CPU SMART FAN



# SYS\_FAN\_1

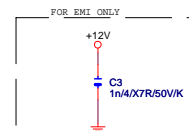
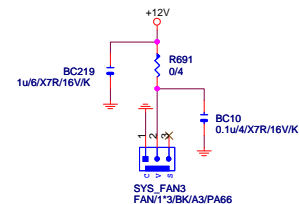


# SYS\_FAN\_2



# SYS\_FAN\_3

Linear SYS\_FAN

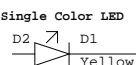
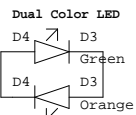
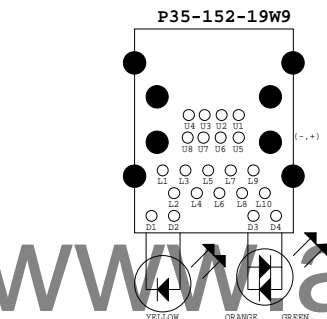
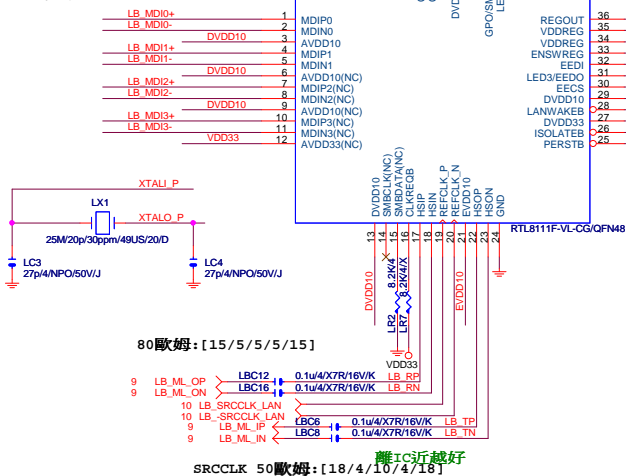


Gigabyte Technology

Title		HWM,KB/MS, FAN CTRL	
Size	Document Number	GA-P85-D3	
Custom	Rev	2.0	
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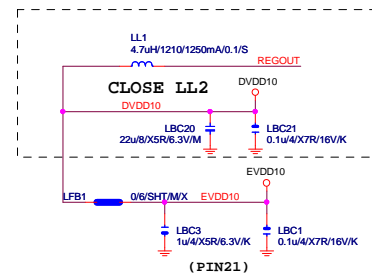
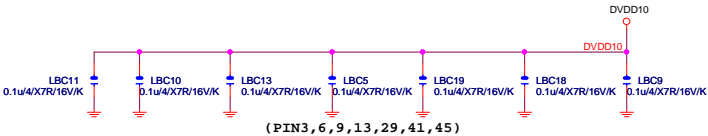
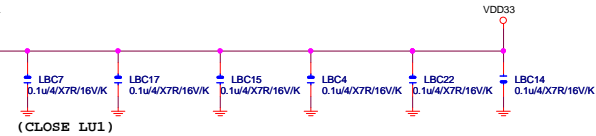
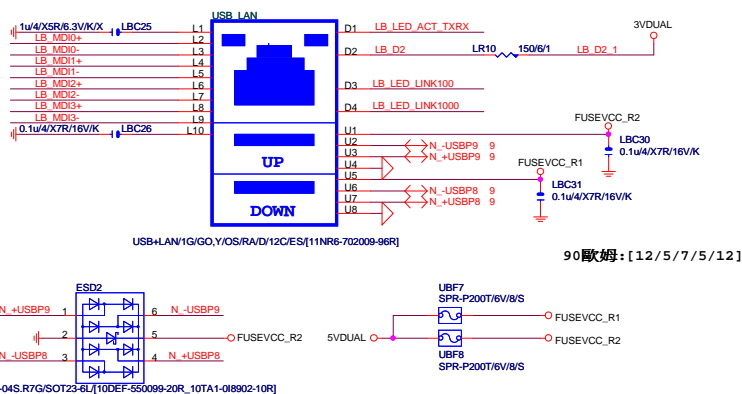
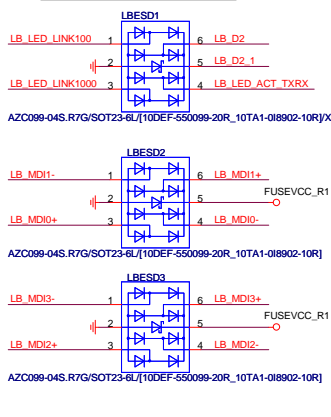
## LAN:INTEL I217

100歐姆:[20/4/8/4/20]

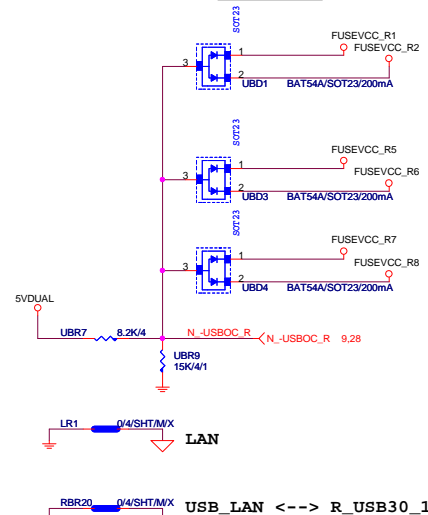


## USB30\_LAN CONNECTOR

100歐姆:[20/4/8/4/20]



## -USB0C\_R

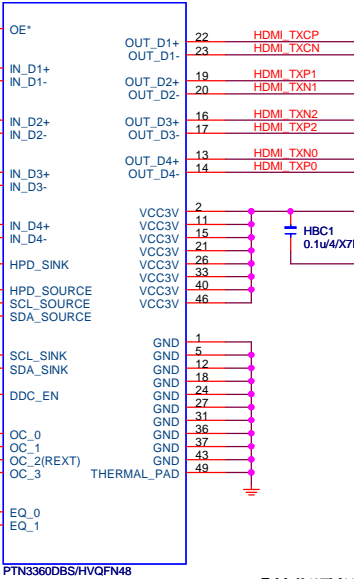


Gigabyte Technology			
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# HDMI LEVEL SHIFT

HDMI:20/4/6/4/20  
Impedance=85 +- 17.5%

HU1



ASM1442  
Default [0,1,0]  
450mv,-3dB

ASM1442 Default [0,0] 3dB  
[0,1]6dB

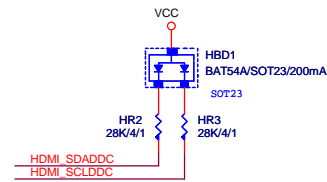
## 【技術通報R&D技術通報150】

HDMI eye diagram1.4版(deep color)會fail

原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram

改善: ASMEDIA ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

www.aitech1.ru



FUSEVCC\_R7

HBC5

1u/4/X5R/6.3V/K

HR4

20K/4/1

HDMI PLUG

HR4

20K/4/1

HBC12

0.1u/4/X7R/16V/K

HBC1

0.1u/4/X7R/16V/K

HBC2

0.1u/4/X7R/16V/K

HBC3

0.1u/4/X7R/16V/K

HBC4

10u/6/X5R/6.3V/M

HBC12

0.1u/4/X7R/16V/K

HBC1

0.1u/4/X7R/16V/K

HBC2

0.1u/4/X7R/16V/K

HBC3

0.1u/4/X7R/16V/K

HBC4

10u/6/X5R/6.3V/M

HBC12

0.1u/4/X7R/16V/K

HBC1

0.1u/4/X7R/16V/K

HBC2

0.1u/4/X7R/16V/K

HBC3

0.1u/4/X7R/16V/K

HBC4

10u/6/X5R/6.3V/M

HBC12

0.1u/4/X7R/16V/K

HBC1

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HBC2

0.1u/4/X7R/16V/K

HBC3

0.1u/4/X7R/16V/K

HBC4

10u/6/X5R/6.3V/M

HBC12

0.1u/4/X7R/16V/K

HBC1

0.1u/4/X7R/16V/K

HBC2

0.1u/4/X7R/16V/K

HBC3

0.1u/4/X7R/16V/K

HBC4

10u/6/X5R/6.3V/M

HBC12

0.1u/4/X7R/16V/K

HBC1

0.1u/4/X7R/16V/K

HBC2

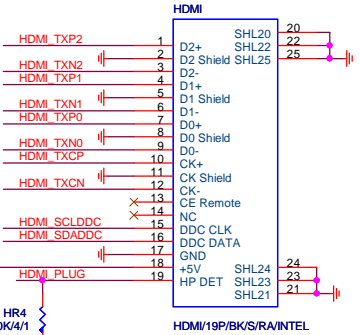
0.1u/4/X7R/16V/K

HBC3

0.1u/4/X7R/16V/K

HBC4

10u/6/X5R/6.3V/M



R9

0/4/SHT/M/X

HBC5

1u/4/X5R/6.3V/K

HR4

20K/4/1

HDMI PLUG

HR4

20K/4/1

HBC12

0.1u/4/X7R/16V/K

HBC1

0.1u/4/X7R/16V/K

HBC2

0.1u/4/X7R/16V/K

HBC3

0.1u/4/X7R/16V/K

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HBC3

0.1u/4/X7R/16V/K

GIGABYTE™			
Title			
HDMI			
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